

Power Distribution Network Design Methodologies

By Istvan Novak

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Power Distribution Network Design Methodologies is a collection of cogently written articles by 49 industry experts that fills in the void on PDN design procedures, and addresses among others such related topics as DC-DC converters, selection of bypass capacitors, DDR2 memory systems, powering of FPGAs, synthesis of impedance rofile. Through each of these contributions from such leading companies as SUN Microsystems, Sanyo, IBM, Hewlett-Packard, Intel, and Rambus, the reader can come to understand why books on power-integrity are only now becoming available to the public and can relate these topics to current industry trends.

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Editorial Review

Review
This book is the most complete, most thorough book that I have ever seen on the rapidly evolving field of power integrity (PI), which includes contributions from nearly 50 noted experts at well-known electronics manufacturers, consultants, and universities. Instead of presenting the one best solution, it offers a variety of viewpoints and discusses the complexity of building products, product cost, and their robustness against design and manufacturing errors. --John Barnes, Owner, dBi Corporation

Dr. Istvan Novak has brought together industry experts of signal integrity and EMI to share their perspectives on power delivery design and experiences in optimizing the design of the power distribution network. This book covers the collected wisdom of the industry as reported over the last 10 years. If your work involves the power delivery network, this is a must-have. --Eric Bogatin, President, Bogatin Enterprises, LLC

A comprehensive book on power distribution networks (PDN) and power integrity (PI) has been in need for some time, and this monumental volume by Dr. Novák is one of the best on this subject. This book brings tremendous and tangible value to anyone who has the interests in, or is already working on, these ever-evolving and intriguing fields. --Mike Peng Li, Principal Architect and Distinguished Engineer, Altera Corporation

About the Author
Istvan Novak is a distinguished engineer of signal and power integrity at Sun Microsystems. He is a fellow of IEEE for his contributions to signal-integrity modeling, measurements, and simulations. Dr. Novak has been working on high-speed signaling and power distribution designs of Sun's V880, V480, V890, V490, T1000, T2000, T5120 and T5220 mid-range server families. His new technology development work with laminate suppliers, printed-circuit fabricators and component vendors resulted in the introduction of the first sub 2-mil laminates and controlled-ESR bypass capacitors for Sun servers. Dr. Novak developed a new validation methodology for the measurement of a wide range of power-distribution components, such as DC-DC converters, bypass capacitors and printed-circuit-board power-ground laminates. The methodology has been presented in several conference papers, two of which won the best paper awards. Dr. Novak carries 24 years of international consulting and instructing experience, and 28 years of design experience in the field of high-speed and high-frequency circuits and systems. He is an international consultant and instructor with 30 years of experience in the field of high-speed and high-frequency circuits and systems. Dr. Novak holds 25 patents in power distribution, signal integrity, and digital signal processing, is the co-author of *Frequency-Domain Characterization of Power Distribution Networks*; and he has published more than 100 technical papers. Dr. Novak has worked and consulted for several companies in the computer and telecommunications industry, to do clock- and power-distribution networks, switching-mode converters as well as various high-speed backplanes, PCI buses, and copper and optical interconnects in the GB/s range. Dr. Novak obtained his Ph.D. degree from the Hungarian Academy of Sciences, and he received his technical education from the Technical University of Budapest.

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